

FIG.5

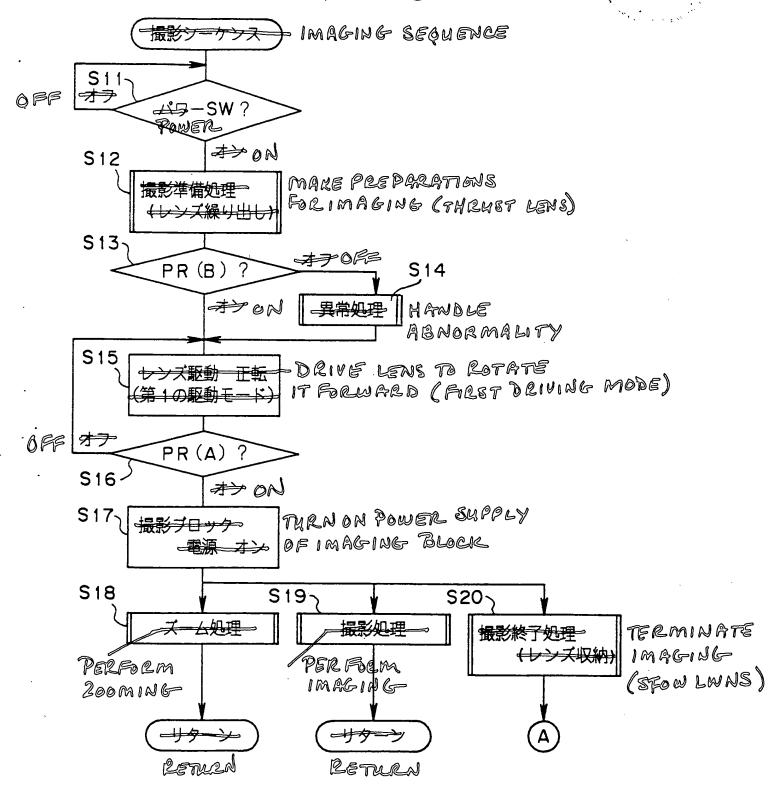
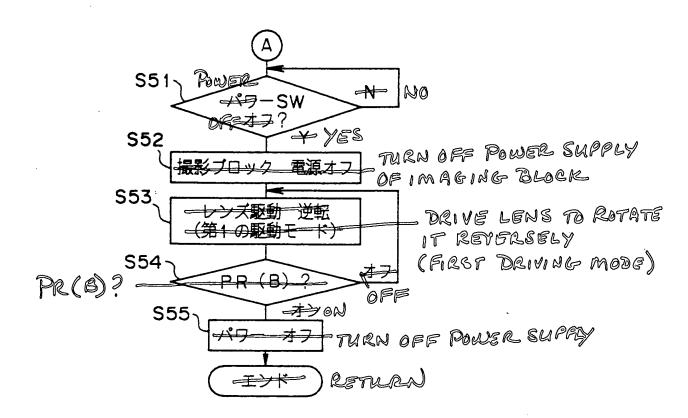


FIG.6



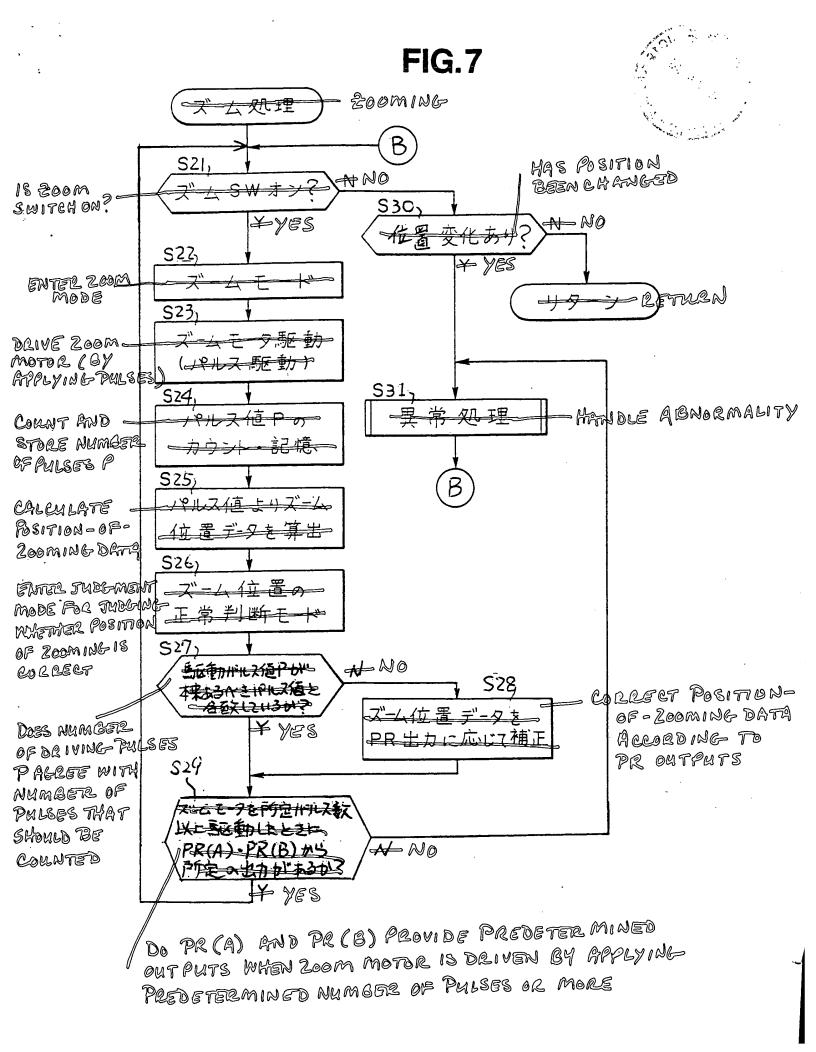


FIG.8

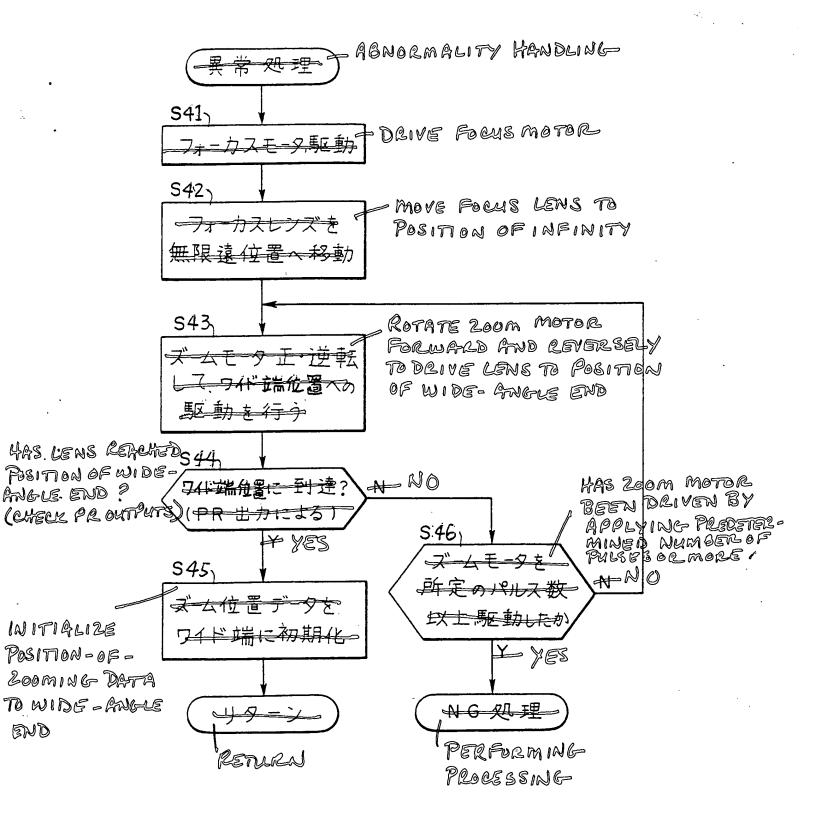


FIG.9

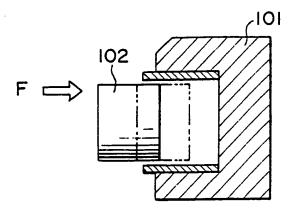


FIG.10

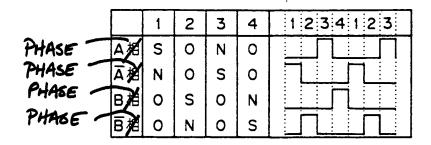


FIG.11

		1	2	3	4	1234123
PHASE	A枢	Z	S	S	Ν	
PHASE	Ā	S	N	N	S	
PHASE	B≱	N	N	S	S	
PHASE	B超	S	S	N	N	

FIG.12

		1	2	3	4	5	6	7	8	1234567812345678
PHASE	AZ	N	N	0	S	S	S	0	N	
PHASE	A 接	s	s	0	N	N	N	0	s	
PHASE	B	0	N	N	N	0	S	s	s	
PHASE	BA	0	s	s	S	0	N	N	N	